

**IN THE CLAIMS**

Please amend the claims to be in the form as follows:

**Claim 1 (original):** A method of controlling an arrangement of a plurality of hardware components, at least some of which are coupled to one another via signal leads, by means of a data processing unit and a computer program which is executed therein, characterized in that the computer program comprises sub-modules (2',3') which correspond to the hardware components (2,3) and are connected via data channels in conformity with the real signal leads between the hardware components (2,3).

**Claim 2 (original):** A method as claimed in Claim 1, characterized in that the sub-modules (2',3') of the computer program and/or the data channels between the sub-modules are adapted in conformity with the dynamic changing of the hardware components and/or the signal leads between the hardware components.

**Claim 3 (original):** A method as claimed in Claim 1, characterized in that all data channels utilize the same communication protocol.

**Claim 4 (original):** A method as claimed in Claim 1, characterized in that the hardware components are printed circuit boards, layout cells, microchips and/or core cells.

**Claim 5 (original):** A data and signal processing device which includes a plurality of hardware components, at least some of which are coupled to one another via signal leads, and also a data processing unit which serves to control the hardware components (2,3) and in which a computer program can be executed, characterized in that the computer program comprises sub-modules (2',3') which correspond to the hardware components (2,3) and are connected via data channels in conformity with the real signal leads between the hardware components (2,3).

**Claim 6 (original):** A data and signal processing device as claimed in Claim 5, characterized in that it is a television set, a video recorder, a set top box or an audio apparatus.

**Claim 7 (new):** A data and signal processing device as claimed in Claim 6, characterized in that the sub-modules have inputs and outputs which correspond to input and outputs for the hardware components.

**Claim 8 (new):** A data and signal processing device as claimed in Claim 7, characterized in that a signals' validity can be reported through the inputs and outputs to the sub-modules and the hardware components.

**Claim 9 (new):** A data and signal processing device as claimed in Claim 7, characterized in that hardware switches can establish connection through the inputs and outputs to the sub-modules and the hardware components.

**Claim 10 (new):** A data and signal processing device as claimed in Claim 7, characterized in that a signals' properties can be defined through the inputs and outputs to the sub-modules and the hardware components.

**Claim 11 (new):** A data and signal processing device as claimed in Claim 10, characterized in that the signals' defined properties can reported to all hardware components through the inputs and outputs to the sub-modules and the hardware components.